



PCB LAYERS:

- 1 ————— SIGNAL
- 2 ————— GND
- 3 ————— SIGNAL
- 4 ————— VCC
- 5 ————— VCC
- 6 ————— SIGNAL
- 7 ————— GND
- 8 ————— SIGNAL

NOTES:

- 1) ALL ICs GET A 0.1 uF CAP NEXT TO THEM.
- 2) MIDDLE STIFFENER BAR MAY BE MOVED VERTICALLY SLIGHTLY TO ACCOMMODATE IC ROWS.
- 3) DRAM SIP ARRAY MUST HAVE A HOLE FOR A #4 SCREW JUST OUTSIDE EACH CORNER FOR MOUNTING THE PROTECTIVE COVER.
- 4) ALL SIGNALS PREFIXED WITH "EC_" OR "EC2_" MUST BE ECL ROUTED FROM THE CONNECTOR, TO THE ICs, AND THEN TO THE TERMINATORS.
- 5) IC ROW HEIGHTS ARE AS FOLLOWS:
 1.3" - B, H, J
 1.4" - ALL OTHERS
- 6) PUT 8. 5.0 uF CAPS NEXT TO THE DRAM ARRAY.
- 7) PLACE 8 - 82 uF DECOUPLING CAPS AS SHOWN.

THE INFORMATION CONTAINED IN THIS DOCUMENT IS CONFIDENTIAL AND PROPRIETARY TO LMI, AND IS NOT TO BE REPRODUCED, DISCLOSED TO OTHERS, OR USED WITHOUT WRITTEN PERMISSION OF AN AUTHORIZED REPRESENTATIVE OF LMI.	PREPARED	DATE	L M I	
	K. HOULT	10/10/86	LISP Machine Inc.	
	CHECKED		6 Tech Drive Andover, MA 01810	
	APPROVED		FALCON MEM_80 LAYOUT INFO	
APPROVED		SIZE	CAT.	REF
APPROVED		D	N/A	
		NO.	2	REV
		SHEET	2 OF 2	0