NOTE: (1) ALL UNMARKED CAPACITORS ARE 0.1 uf.
ASSEMBLY DIAGRAM

TRANSISTORS
Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8
C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15
R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15

DIODES
D1, D2, D3, D4, D5, D6, D7, D8

VOLTAGE REGULATORS
V1, V2 - DUAL ZENER 7.5 V
V3 - 5 VOLT REG 3.3 V

PIN ASSIGNMENTS

Intertec Data Systems
Mega
CDC 9409T DISK DRIVE

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES, TOLERANCES ARE
FRACTIONS AS DECIMALS ANGLES

CONTRACT NO.

APPROVALS

DATE

MATERIAL

DRAWN

CHECKED

APPLICATION

DO NOT SCALE DRAWING

SCALE

SHEET OF

B

S140

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