SPECIFICATION
FGSV11 LSI-11 Memory Boards
1, 2, 3 and 4 Megabyte Capacities

Specifications

Capacity
FGSV11-1: 1 Mbyte + parity
FGSV11-2: 2 Mbytes + parity
FGSV11-3: 3 Mbytes + parity
FGSV11-4: 4 Mbytes + parity

Compatibility
Any 22 bit Q-bus system
The FGSV11 supports DEC's new fast DMA protocol and is functionally equivalent to DEC's MSV11-PL.

Addressing
Any 1 Mbyte boundary within the LSI-11/23-PLUS 4 Mbyte addressing range. Starting address block selected by on-board DIP switch.

Parity
On board Control/Status register

Power
Single + 5V supply
FGSV11-1: 2.5A typical (maximum 4.2A)
FGSV11-4: 3.2A typical (maximum 6.2A)

Space requirement
One DEC quad-height slot

Speed
Access time (SYNCH to RPLYH on DATI or DATO(B) cycles): typically 225ns (250ns maximum)
Block mode DMA cycles (DINH or DOUTH to RPLYH): typically 120ns (150ns maximum)

Memory chips
FGSV11-1: 64K x 1 bit dynamic RAM
FGSV11-2, 3, 4: 256K x 1 bit dynamic RAM
Both boards use industry standard DRAM chips so that replacements are available from multiple sources.

Warranty
1 year with despatch of repaired board or loan replacement within 24 hours of faulty board being received at Perth factory.

Circuit board
High reliability Multiwire technology board

Availability
Maximum 30 days from receipt of order