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UNIT VARIATIONS COVERED BY THIS PRINT SET

DDVII-B

Field Maintenance
Print Set

Digital Equipment
Corporation

PRINT SET ORDER NO.
MP58122
DO NOT SCALE DRAWING

SEE NOTE I — SEE DETAIL A

POWER SIGNAL PINS

PIN A01A1

DETAIL A

SLOT

12V GND +5B +5V +12V

GND

NOTE:
1. BE SURE STAKE PINS HAVE BEEN INSERTED IN ITEM NO. 4 BEFORE WAVE SOLDER.

CIRCUIT SCHEMATIC

DDVII-B

PARTS LIST

DIMENSIONAL TOLERANCE

UNLESS OTHERWISE SPECIFIED

MATERIAL:

SEE PARTS LIST

FINISH

SCALE

SHEET 1 OF
NOTES:
1. SHOULD AVOID WIRING FROM USER AREA TO LSI-II BUS. THIS WILL ADD ADDITIONAL CAPACITANCE ON BUS SIGNAL LINES AND MAY CHANGE THE AC GAIN OF THE LSI-II BUS SPEC.
2. POWER AND GND CONNECTIONS PROVIDED AT THE FOLLOWING PINS:
   1\*24: 1ZV, 21Z
   1\*26: GND, 22Z

VIEW FROM PIN SIDE

USER AREA (SLOTS 1-9, ROW A, B, C, AND D)
1. THESE SLOTS ARE USER DEFINED AND NOT CONSIDERED TO BE PART OF LSI-BUS. SEE NOTE 1.
2. POWER AND GROUND CONNECTION PROVIDED. SEE NOTE 2.

LSI-II BUS (SLOTS 1-9, ROW A, B, C, AND D)
GENERAL
1. ANY OPTION MEETING LSI-II BUS SPEC CAN BE PLUGGED INTO THESE SLOTS.

NOTE: HENCE
A. THE LSI-II SLOT BUS MAY NEED ADDITIONAL BUS TERMINATING:
   M74H304-4D (PWR-10G) TERMINATING MODULE.
B. SLOT 1 IS USUALLY RESERVED FOR CPU MODULE (M7624A).